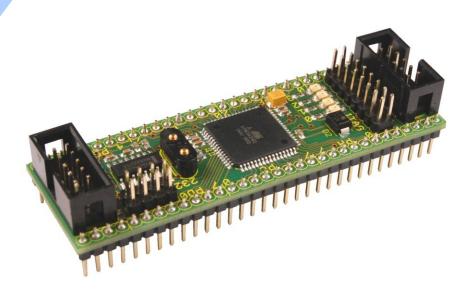


AVR-Development Module

Model: AL-AVREB_256

- Summary
- Measures
- Description
- Electrical Characteristics
- Programming
- Settings





Summary

LED0/PF0 LED1/PF1 LED2/PF2 LED3/PF3 JTAG-TCK/PF4 JTAG-TMS/PF5 JTAG-TDO/PF6 JTAG-TDI/PF7 ISP MOSI/RS232 RXD0/PE0 ISP MISO/RS232 TXD0/PE1 PE2 PE3 붟ô빙ຣ평왕췽퀭혦볞相 PE4 PE5PE6 PE7 PB0 **ISP SCK/PB1** PB2 PB3 PB4 $\mathbf{PB5}$ **PB6 PB7** PG5 **BOOT-PIN/PG4** PG3 PG2 PG1 PG0 GND GND

RESET AREF PA0 PA1 PA2 PA3 PA4 PA5 PA6 PA7 PC7 PC6 PC5 PC4 PC3 PC2 PC1 PC0 PD7 PD6 PD5 PD4 PD3/RS232 TXD1 PD2/RS232 RXD1 PD1 PD0 OUT0 OUT1 IN0

IN1

vcc

vcc

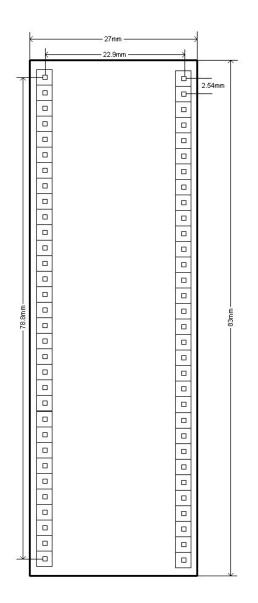
All description in **BLUE** concern the internal connection

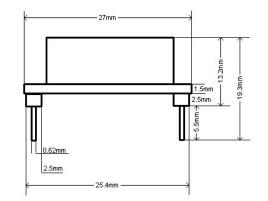
Attention! Polarity reversal and overvoltage may cause a destruction of the electronic components!!!



Measures

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Description

- Controller: Atmel AVR ATmega2561-16AU up to 16 MHz
- Supply voltage: 5V
- Module size: W x H x D 27mm x 83mm x 19.3mm
- Quartz socket: simple and fast quartz exchange
- PC-Connection: 2 x RS232, separable with jumpers
- Compatibility: compatible with IC-Socket 64-pin and hole matrix board
- Pin-Distance: 2.54 mm
- LED: 4 LEDs, separable with jumpers
- Circuit: built on the recommendation of the manufacturer
- Programming: ISP or JTAG connector
- Pin configuration of AVR-Module: shown at the left picture
- Pin configuration ISP & JTAG connectors: 10-pin, standard of Atmel
- Functionality: tested, ready for use
- Conformity: RoHS Compliance
- Produced in Germany
- Description:

We offer you more flexibility by the development. By means of quartz socket it is possible to choose another frequency easier and faster. IC-Socket makes possible the fast installation of AVR-Module and fits the hole matrix board with the hole distance 2.54 mm. All pins of micro controller are connected with the pins of module and positioned in the logical order, that makes the development work easier. The circuit of the module is built on the recommendation of the manufacturer: A/D converter, reset, ISP, JTAG, RS232, LEDs. Jumper configuration helps you to make the right settings. A suppressor diode is responsible for the security of AVR-Module. We offer you a very simple installation and use of AVR-Module for the beginner as well as for the advancer.

| 1_PF0 2_PF1 3_PF2 4_PF3 5_PF4 6_PF5 7_PF6 8_PF7 9_PE0 10_PE1 11_PE2 12_PE3 13_PE4 14_PE5 16_PE7 15_PE6 16_PE7 17_PB0 18_PB1 19_PB2 20_PB3 21_PB4 22_PB5 23_PB6 24_PB7 25_PG5 26_PG4 27_PG3 28_PG2 29_PG1 30_PG0 31_GND 32_GND | 64_VCC 63_VCC 62_RESET 61_AREF 60_PA0 59_PA1 58_PA2 57_PA3 56_PA4 55_PA5 54_PA6 53_PA7 54_PA6 53_PA7 54_PA6 53_PA7 54_PA6 54_PA6 53_PA7 54_PA6 54_PC1 45_PC1 45_PC2 44_PD7 43_PD6 42_PD5 44_PD7 43_PD6 42_PD5 33_PD2 33_PD1 37_PD0 35_2320UT1 34_2320UT 34_2320UT1 | |
|---|--|--|
| 32_GND | 33_232IN1 | |
| | | |



Electrical Characteristics

| | L | | | |
|------------------------|----------|---------|------------------|-------|
| for <u>all</u> modules | s with | Ope | erating Temperat | ture |
| MAX3232EID | (actual) | 40.90 | | 95.00 |
| MAX3232IDR | (actual) | - 40 °C | | 85 °C |
| MAX202ECSE | | 0.00 | | 70.00 |
| MAX3232ECD | | 0 °C | | 70 °C |

Min

Тур

Max

| | Operating Voltage | | |
|-------------------------|-------------------|--|-------|
| • with AT mega2561-16AU | 4.5 V | | 5.5 V |
| • with AT mega2561V-8AU | 3.0 V | | 5.5 V |

| | Taktfrequenz | | |
|-------------------------|--------------|--|--------|
| • with AT mega2561-16AU | 0 Hz | | 16 MHz |
| • with AT mega2561V-8AU | 0 Hz | | 8 MHz |

| | Maximum DC Current per I/O Pin | | |
|-------------------------|--------------------------------|-------|--|
| • with AT mega2561-16AU | | 20 mA | |
| • with AT mega2561V-8AU | | 10 mA | |

more electrical characteristics you will find on the page 370 in the data sheet ATmega2561.pdf

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- ► Voltage Suppressor P6SMB6.8A
- ► 2-layer PCB DIN ISO 9001
- ▶ with UL-Approbation
- ► 4x LED yellow 2V 20 mA 140° 39 mcd

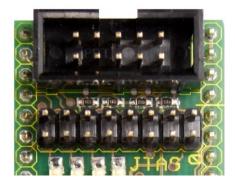
Possible Modifications

with ATmega2561V-8AU
with mounted quartz (without quartz socket)
without laterally pins

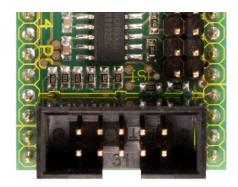


Programming

JTAG¹



ISP²



Pin Configuration JTAG-Connector

Pin Configuration ISP-Connector

| (9) TDI | (7) VCC | (5) TMS | (3) TDO | (1) TCK | v |
|-------------|------------|--------------|------------|------------|---|
| (10) GND | (8) | (6) Reset | (4) VCC | (2) GND | Μ |

| (2) | (4) | (6) | (8) | (10) |
|------|-----|-------|-----|------|
| VCC | GND | GND | GND | GND |
| (1) | (3) | (5) | (7) | (9) |
| MOSI | GND | Reset | SCK | MISO |

- 1 When programming with JTAG the JPI-(1-4)-jumpers should be set.
- 2 When programming with ISP the UART-jumpers JP2-3 and JP2-1 should not be set.



Settings

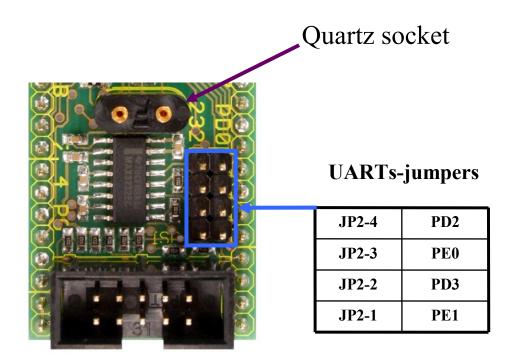
| JTAG-jumpers JP1-(When programming with JTAG the jump should be set in the re- square. | | | | | | | |
|---|--|--|--|--|--|--|--|
| | | | | | | | |
| LED-jumper 3 +LED 3 (yellow) +resistor | LED-jumper 2 +LED 2 (yellow) +resistor | LED-jumper 1 +LED 1 (yellow) +resistor | LED-jumper 0 +LED 0 (yellow) +resistor | | | | |
| Jumper is connected to the pin PF3 | Jumper is connected to the pin PF2 | Jumper is connected to the pin PF1 | Jumper is connected to the pin PF0 | | | | |

Jumpers set-up

| JP1-8 | JP1-7 | JP1-6 | JP1-5 | JP1-4 | JP1-3 | JP1-2 | JP1-1 |
|------------------|-------|-------|-------|----------------|----------------|----------------|----------------|
| LED 3 Pin PF3 | | | | TDI Pin PF7 | TDO Pin PF6 | TMS Pin PF5 | TCK Pin PF4 |

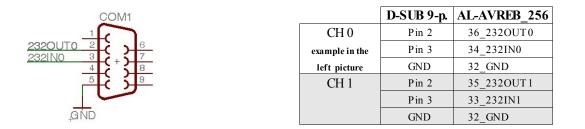
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The UARTs-pins can be parted from RS232 transceivers with these jumpers. When programming with ISP the UART-jumpers JP2-3 (PE0) and JP2-1 (PE1) should not be set.

Connection of D-SUB 9-pin female connector (serial port/COM1)



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